



FPGA Platform Design Engineer

Job Description

Responsibilities include (but are not limited to):

- Responsible for the design, implementation, and verification of FPGA solution on AMD's Servers
- Collaboration and hands-on debug with other Server Teams (BIOS, BMC, Platform, etc.)
- Review platform schematics & BMC code base to implement FPGA features for Server Platform bring up
- Leading the bring up of your FPGA solution and working in a collaborate with the team on debug

Minimum Qualifications:

- Proficiency with Verilog and/or System Verilog
- Strong RTL Design experience for FPGA or ASIC
- Proficient Experience with FPGA Synthesis, Timing Closure, Simulation and HW Bring up
- Familiarity with programming/scripting languages (Python,, TCL, Bash, C, Makefile, etc.)
- Understanding of one or more protocols: SPI, I2C, AXI, QSPI, USB, etc.
- Experience with lab equipment (protocol/logic analyzers, oscilloscopes, etc.)
- Experience with source control systems (Git)
- Strong verbal and written English communication skills
- Able to execute and drive success of programs with multiple projects on the go
- Must be a self-starting team player with excellent communication skills who can work with minimal guidance
- Bachelor's degree or higher in Electrical/Computer Engineering or Electronics / Computer Science related
- 8+ years of experience in FPGA Design + Bring up Experience