

## **Post Silicon Validation Engineer**

### **Job Description**

Responsibilities include (but are not limited to):

- Experience in platform-level validation of various domains – High Speed IO, memory, thermal/power.
- Able to write test plans to expand functional test coverage.
- Experience in Python or C/C++ programming.
- Debug and drive root-cause analysis for SoC related issues.
- Debug skills at both SoC and system level.
- Familiarity with embedded system and assembly code level debugging.
- x86 architecture and OS knowledge will be an added advantage.

### **Minimum Qualifications:**

- Minimum of a Bachelor's degree in Electronic/Electrical Engineering, Computer Engineering, Post Graduate Diploma, Professional Degree, Computer Science/Information Technology, Engineering (Computer/Telecommunication), Engineering (Electrical/Electronic), Engineering (Mechatronic/Electromechanical) or equivalent.
- Required skill(s): Python, x86, Os, C Language, C++ Language.
- At least >3 year(s) of working experience in the related field is required for this position.
- Applicants must be willing to work in Bayan Lepas.
- Preferably Senior Executives specializing in Engineering - Electrical or equivalent.